

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S16 4	49968	(shift adj register)with (phase or pulse or clock)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 13:35
S16 6	6077	(shift adj register)with (phase or pulse or clock) and (ccd or photodiode or photoelectric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 13:36
S16 5	5178	(shift adj register)with (phase or pulse or clock) and (ccd or photodiode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 13:36
S16 7	586	(shift adj register)with (phase or pulse or clock) with (third or three) and (ccd or photodiode or photoelectric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 13:39
S16 8	351	(shift adj register)with (phase or pulse or clock) near4 (third or three) and (ccd or photodiode or photoelectric)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 14:21
S16 9	252	(shift adj register)with (phase or pulse or clock) near4 (third or three) and (ccd or photodiode or photoelectric)and light	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 16:46
S17 0	204	(shift adj register)with (phase or pulse or clock) near4 (third or three) and (ccd or photodiode or photoelectric)and frequenc\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 16:47
S17 2	48	(shift adj register)with (phase or pulse or clock) near4 (third or three) and (ccd or photodiode or photoelectric)and (frequenc\$4 with (phase or pulse or clock)with (two or twice or double))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 16:49
S17 1	163	(shift adj register)with (phase or pulse or clock) near4 (third or three) and (ccd or photodiode or photoelectric)and (frequenc\$4 with (phase or pulse or clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/19 16:49

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S17 3	10	("4281254").URPN.	USPAT	OR	OFF	2006/10/20 08:00
S17 4	296	three adj phase with shift adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 08:01
S17 5	79	three adj phase with shift adj register and accumulat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 08:37
S17 6	32	three adj phase with shift adj register and (combin\$4 with charge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 08:42
S17 7	26	three adj phase with shift adj register and ((added or adding or add) with charge)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:17
L1	2039	digital adj camera with analog with digit\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:18
L2	74	digital adj camera with analog with digit\$5 with gain	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:19
L5	1	digital adj camera same analog with digit\$5 with gain same shading	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:20
L4	1	digital adj camera same analog with digit\$5 with gain with shading	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:20

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L3	1	digital adj camera with analog with digit\$5 with gain with shading	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:20
L6	8	digital adj camera same analog with digit\$5 with gain and shading	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:21
L7	8	digital adj camera same analog with digit\$5 with gain with correction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:22
L8	41	digital adj camera and analog with digit\$5 with gain with correction	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:25
L11	2111	shift adj register and (transfer near4 mode)and frequenc\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:26
L10	3390	shift adj register and (transfer near4 mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:26
L9	120783	shift adj register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:26
L13	0	shift adj register and (transfer near4 mode)with (frequenc\$4 with (twice or double))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:27

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L12	255	shift adj register and (transfer near4 mode)and (frequenc\$4 with (twice or double))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:27
L14	2	shift adj register and (transfer near4 mode)same (frequenc\$4 with (twice or double))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:28
L15	201	shift adj register and (frequenc\$4 with (twice or double)) with resolution	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:38
L16	14	shift adj register and (frequenc\$4 with (twice or double)) with ((low or high) near4 resolution)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:42
L17	5	shift adj register and (frequenc\$4 with (twice or double)) with ((low or high) near4 resolution)and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:48
L19	129	shift adj register and (frequenc\$4 with (twice or double)) with resolution with nyquist and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 10:49
L20	816	shift adj register and (frequenc\$4 with (twice or double)) with pulse	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:02
L18	143	shift adj register and (frequenc\$4 with (twice or double)) with resolution and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:02

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L21	9	shift adj register and (frequency\$4 with (twice or double)) with pulse with mode	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:07
L23	2224	shift adj register and (frequency\$4 with (twice or double)) with (pulse or clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:09
L22	56	shift adj register and (frequency\$4 with (twice or double)) with (pulse or clock) with mode	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:09
L24	219	shift adj register with (frequency\$4 with (twice or double)) with (pulse or clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:10
L26	8429	shift adj register with (phase)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:11
L25	52336	shift adj register with (pulse or clock or phase)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:11
L27	296	shift adj register with (three adj phase)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:12
L28	114	shift adj register with (three adj phase) and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:25

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L29	69	shift adj register with (phase with frequenc\$4) and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:28
L30	1	shift adj register with (phase with frequenc\$4 with (twice or double)) and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:30
L31	2	shift adj register same (phase with frequenc\$4 with (twice or double)) and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:36
L34	143	shift adj register and (frequenc\$4 with (twice or double))with resolution and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:40
L33	0	shift adj register and (phase with frequenc\$4 with (twice or double))with resolution and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:40
L32	26	shift adj register and (phase with frequenc\$4 with (twice or double)) and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 11:40
L36	2758	horizontal near2 shift near2 register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:12
L39	7	horizontal near2 shift near2 register with frequenc\$4 with (doubl\$1 or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:13

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L38	7	horizontal near2 shift near2 register with frequenc\$4 with (double or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:13
L37	133	horizontal near2 shift near2 register with frequenc\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:13
L40	9	horizontal near2 shift near2 register with frequenc\$4 with (doubl\$2 or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:19
L41	18	horizontal near2 shift near2 register same frequenc\$4 with (doubl\$2 or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:00
L42	4845	shift adj5 register and mode with frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:02
L43	182	shift adj5 register and mode with frequency with (twice or doubl\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:25
L44	29	shift adj5 register and mode with frequency with (twice or doubl\$5)and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 15:22
L45	770	382/312.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 15:23

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L40	9	horizontal near2 shift near2 register with frequenc\$4 with (doubl\$2 or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 13:19
L41	18	horizontal near2 shift near2 register same frequenc\$4 with (doubl\$2 or twice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:00
L42	4845	shift adj5 register and mode with frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:02
L43	182	shift adj5 register and mode with frequency with (twice or doubl\$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 14:25
L44	29	shift adj5 register and mode with frequency with (twice or doubl\$5)and ccd	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 15:22
L45	770	382/312.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 15:23


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1. **A 2/3" 1188(H) * 484(V) frame-transfer CCD for ESP and movie mode**

Bosiers, J.; Dillen, B.; Jaspers, C.; Kleimann, A.; Kokshoorn, A.; Peek, H.; van Electron Devices Meeting, 1988. Technical Digest., International 11-14 Dec. 1988 Page(s):70 - 73 Digital Object Identifier 10.1109/IEDM.1988.32753

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2. **A 2/3-In 1187(H)×581(V) S-VHS-compatible frame-transfer CCD for ESP an**

Bosiers, J.T.; Kleimann, A.C.; Dillen, B.G.; Peek, H.L.; Kokshoorn, A.L.; Daem Sijde, A.G.; van Gaal, L.T.; Electron Devices, IEEE Transactions on Volume 38, Issue 5, May 1991 Page(s):1059 - 1068 Digital Object Identifier 10.1109/16.78380

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3. **Application of charge-coupled devices to Infrared detection and Imaging**

Steckl, A.J.; Nelson, R.D.; French, B.T.; Gudmundsen, R.A.; Schechter, D.; Proceedings of the IEEE

Volume 63, Issue 1, Jan. 1975 Page(s):67 - 74

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4. **An experimental study of charge diffusion in the undepleted silicon of X-**

Prigozhin, G.; Butler, N.R.; Kissel, S.E.; Ricker, G.R.; Electron Devices, IEEE Transactions on

Volume 50, Issue 1, Jan. 2003 Page(s):246 - 253 Digital Object Identifier 10.1109/TED.2002.806470

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5. **Lynx: a high-resolution synthetic aperture radar**

Tsunoda, S.I.; Pace, F.; Stence, J.; Woodring, M.; Hensley, W.H.; Doerry, A.W. Aerospace Conference Proceedings, 2000 IEEE

Volume 5, 18-25 March 2000 Page(s):51 - 58 vol.5 Digital Object Identifier 10.1109/AERO.2000.878471

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Electron Devices Meeting, 1988. Technical Digest., International
11-14 Dec. 1988 Page(s):70 - 73
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2. **A 2/3-In 1187(H)×581(V) S-VHS-compatible frame-transfer CCD for ESP and**
Bosiers, J.T.; Kleimann, A.C.; Dillen, B.G.; Peek, H.L.; Kokshoorn, A.L.; Daem, Sijde, A.G.; van Gaal, L.T.;
Electron Devices, IEEE Transactions on
Volume 38, Issue 5, May 1991 Page(s):1059 - 1068
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3. **Application of charge-coupled devices to infrared detection and Imaging**
Steckl, A.J.; Nelson, R.D.; French, B.T.; Gudmundsen, R.A.; Schechter, D.;
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4. **An experimental study of charge diffusion In the undepleted silicon of X-1**
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5. **Lynx: a high-resolution synthetic aperture radar**
Tsunoda, S.I.; Pace, F.; Stence, J.; Woodring, M.; Hensley, W.H.; Doerry, A.W.
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